GENERAL REVOCATION AND POWER OF ATTORNEY
CERTIFICATE UNDER 37 CFR § 3.73(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05 and 402.07, please revoke any existing Powers of Attorney, if any, in each of the patent applications identified on the attached Appendix, and appoint the attorneys and/or patent agents associated with the following Customer Number to prosecute each of the patent applications identified on the attached Appendix and to transact all business in the Patent and Trademark Office in connection therewith:

Customer Number 57246

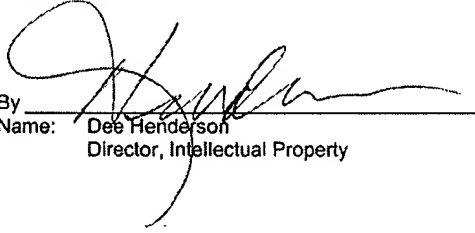Please direct all correspondence in this case to:

Customer Number 57246

CERTIFICATE UNDER 37 CFR § 3.73(b)

Broadcom Corportion hereby certifies that it is the assignee of the entire right, title and interest in each of the patent applications identified in the attached Appendix by virtue of an assignment from the inventor(s) filed and recorded at the Reel/Frame numbers, as noted in the attached Appendix. To the best of my knowledge and belief, title in each of the patent applications identified on the attached Appendix is in Broadcom Corporation, the assignee.

Pursuant to 37 C.F.R. §3.73(b), I hereby declare that I am empowered to sign this certificate on behalf of Broadcom Corporation, the assignee.

Date __2/20/09__

By _____
Name: Dee Henderson
Director, Intellectual Property

APPENDIX

Docket Number	Serial Number	Title	Filing Date	Reg/Frame	INVENTORS
0063-004001	09/599,525	APARATUS AND METHOD FOR OPTIMIZING ACCESS TO MEMORY	23-Jun-2000	011262/0400	Joseph Herbst, Allan Flippin
0063-005001	09/602,474	APPARATUS AND METHOD FOR MANAGING MEMORY IN A NETWORK SWITCH	23-Jun-2000	011265/0994	Joseph Herbst
0063-007004	09/731,025	MIRRORING IN A STACKED NETWORK SWITCH CONFIGURATION	07-Dec-2000	011671/0456	Mohan Kalkunte, Anders Johnson, Shekhar Ambe
0063-008001	09/528,167	METHOD FOR LOAD BALANCING IN A NETWORK SWITCH	17-Mar-2000	010847/0991	Shiri Kadambi, Mohan Kalkunte, Shekhar Ambe
0063-009007	09/528,434	NETWORK SWITCH	17-Mar-2000	010851/0672	Shiri Kadambi, Shekhar Ambe, Mohan Kalkunte
0063-009008	10/799,695	NETWORK SWITCH MEMORY INTERFACE CONFIGURATION	15-Mar-2004	010851/0651	Shiri Kadambi, Shekhar Ambe
0063-009010	11/976,862	NETWORK SWITCH MEMORY INTERFACE CONFIGURATION	29-Oct-2007	010851/0651	Shiri Kadambi, Shekhar Ambe
0063-009011	10/965,260	NETWORK SWITCH	15-Oct-2004	010851/0672	Shiri Kadambi, Shekhar Ambe, Mohan Kalkunte
0063-012001	09/577,012	APPARTUS AND METHOD FOR DISTRIBUTING A LOAD ACROSS A TRUNK	24-May-2000	010221/0448	Shiri Kadambi, Shekhar Ambe
0063-013001	09/528,000	METHOD FOR MANAGING CONJESTION IN A NETWORK SWITCH	17-Mar-2000	010916/0536	Shiri Kadambi, Mohan Kalkunte, Shekhar Ambe
0063-014001	09/654,587	APPARATUS AND METHOD FOR ENABLING VOICE OVER IP SUPPORT FOR A NETWORK SWITCH	01-Sep-2000	011075/0397	Kar-Wing Edward Lor, Mohan Kalkunte, Shekhar Ambe
0063-016001	09/528,164	APPARATUS AND METHOD FOR SORTED TABLE BINARY SEARCH ACCELERATION	17-Mar-2000	010868/0758	Paul Kalapathy, Michael A. Jorda
0063-017002	10/418,280	NETWORK SWITCHING ARCHITECTURE WITH MULTIPLE TABLE SYNCHRONIZATION, AND FORWARDING OF BOTH IP AND IPX PACKETS	18-Apr-2003	010221/0666	SHIRI KADAMBI, SHEKHAR AMBE
0063-019001	09/527,855	METHOD FOR SENDING A SWITCH INDICATOR TO AVOID OUT-OF-ORDERING OF FRAMES IN A NETWORK SWITCH	17-Mar-2000	013276/0242	Shiri Kadambi, Mohan Kalkunte, Shekhar Ambe

0063-019003	09/618,114	ADDRESS RESOLUTION SNOOP SUPPORT FOR CPU	17-Jul-2000	010948/0536	Govind Malalur
0063-020001	09/618,115	FLEXIBLE DMA DESCRIPTOR SUPPORT	17-Jul-2000	010948/0552	Govind Malalur
0063-020003	10/994,637	FLEXIBLE DMA DESCRIPTOR SUPPORT	23-Nov-2004	010948/0552	Govind Malalur
0063-022003	09/539,602	APPARATUS FOR ETHERNET PHY/MAC COMMUNICATION	31-Mar-2000	010933/0517	John K. Lenell
0063-219001	09/602,473	APPARATUS AND METHOD FOR MANAGING MEMORY DEFECTS	23-Jun-2000	011256/0638	Joseph Herbst
0063-219002	10/910,520	APPARATUS AND METHOD FOR MANAGING MEMORY DEFECTS	03-Aug-2004	011256/0638	Joseph Herbst
0063-220001	09/599,544	APPARATUS AND METHOD FOR CONTROLLING DATA FLOW IN A NETWORK SWITCH	23-Jun-2000	011155/0548	Joseph Herbst
0063-221001	09/599,526	APPARATUS AND METHOD FOR STORING DATA	23-Jun-2000	011160/0171	Joseph Herbst
0063-222001	09/527,856	METHOD FOR AVOIDING OUT-OF-ODERING OF FRAMES IN A NETWORK SWITCH	17-Mar-2000	010870/0515	Shiri Kadambi, Mohan Kalkunte, Shekhar Ambe
0063-223001	09/577,012	APPARTUS AND METHOD FOR DISTRIBUTING A LOAD ACROSS A TRUNK	24-May-2000	010221/0448	Shiri Kadambi, Shekhar Ambe
0063-014002	11/433,387	APPARATUS AND METHOD FOR ENABLING VOICE OVER IP SUPPORT FOR A NETWORK SWITCH	15-May-2006	011075/0397	Kar-Wing Edward Lor, Mohan Kalkunte, Shekhar Ambe
0063-224001	09/527,855	METHOD FOR SENDING A SWITCH INDICATOR TO AVOID OUT-OF-ORDERING OF FRAMES IN A NETWORK SWITCH	17-Mar-2000	013276/0242	Shiri Kadambi, Mohan Kalkunte, Shekhar Ambe
0063-225001	09/606,200	NETWORK SWITCH ENHANCEMENT Title Not Matching	29-Jun-2000	010904/0551	Govind Malalur
0063-225002	09/606,210	HIGH-SPEED STATS GATHERING IN A NETWORK SWITCH	29-Jun-2000	010908/0725	Govind Malalur
0063-226002	10/173,414	UNIFIED TABLE FOR L2, L3, L4 SWITCHING AND FILTERING	18-Jun-2002	010962/0815	Shiri Kadambi, Mohan Kalkunte, Shekhar Ambe
0063-227001	09/712,130	NETWORK SWITCH WITH HIGH-SPEED SERIALIZING/ DESERIALIZING HAZARD-FREE DOUBLE DATA RATE SWITCH	15-Nov-2000	011291/0789	Michael J. Bowes
0063-228001	09/897,660	DYNAMIC NETWORK LOAD BALANCING OVER HETEROGENEOUS LINK SPEED	02-Jul-2001	011963/0839	Kan Frankie Fan

0063-032005	10/288,417	SWITCH FABRIC WITH PATH REDUNDANCY	06-Nov-2002	011916/0576	Mohan Kalkunte, Srinivas Sampath, Daniel Tai, Soma Pullela, Kevin Cameron
0063-229002	11/110,906	METHOD AND CIRCUITS FOR STACKING BUS ARCHITECTURE	21-Apr-2005	(Assignment 1-011721/0451)	XI Chen, Brian Chang
0063-049003	10/965,074	METHOD AND APPARATUS FOR PERFORMING A BINARY SEARCH OF AN EXPANDED TREE	15-Oct-2004	(Assignment 2-015571/0985) 011850/0393	Jun Cao, Brandon Carl Smith, Eric Ng
0063-050001	10/086,725	METHOD AND APPARATUS FOR PERFORMNG DIAGNOSTIC TESTS USING AN ASSISTING TRANSCEIVER	04-Mar-2002	012657/0176	Sang T. Bui
0063-050002	11/416,108	METHOD AND APPARATUS FOR PERFORMNG DIAGNOSTIC TESTS USING AN ASSISTING TRANSCEIVER	03-May-2006	012657/0176	Sang T. Bui
0063-057002	11/180,628	NETWORK INTERFACE USING PROGRAMMABLE DELAY AND FREQUENCY DOUBLER	14-Jul-2005	012705/0612	Jonathan Lin, Yong Jiang
0063-058002	11/149,182	NETWORK INTERFACE WITH DOUBLE DATA RATE AND DELAY LOCKED LOOP	10-Jun-2005	012643/0136	Jonathan Lin, Yong Jiang
0063-058003	11/580,956	NETWORK INTERFACE WITH DOUBLE DATA RATE AND DELAY LOCKED LOOP	16-Oct-2006	012643/0136	Jonathan Lin, Yong Jiang
0063-230001	10/032,063	SYSTEM AND METHOD OF IMPROVING MEMORY YEILD IN FRAME BUFFER MEMEORY USING FAILING MEMORY LOCATION	31-Dec-2001	(Assignment 1 012421/0922) (Assignment2-015571/0985)	Shrjie Tzeng
0063-230002	11/171,256	SYSTEM AND METHOD OF IMPROVING MEMORY YIELD IN FRAME BUFFER MEMORY USING FAILING MEMORY LOCATION	01-Jul-2005	(Assignment 1-012421/0922) (Assignment 2-015571/0985)	Shrjie Tzeng
0063-064002	11/176,353	SWITCH ARCHITECTURE INDEPENDENT OF MEDIA	08-Jul-2005	012618/0914	Shiri Kadambi, Shekhar Ambe, Sandeep Relan
0063-064003	11/401,248	A NETWORK SWITCH ARCHITECTURE FOR PROCESSING PACKETS INDEPENDENT OF MEDIA TYPE OF CONNECTED PORTS	11-Apr-2006	012618/0914	Shiri Kadambi, Shekhar Ambe, Sandeep Relan
0063-064004	12/292,323	A NETWORK SWITCH ARCHITECTURE FOR PROCESSING PACKETS INDEPENDENT OF MEDIA TYPE OF CONNECTED PORTS	17-Nov-2008	012618/0914	Shiri Kadambi, Shekhar Ambe, Sandeep Relan
0063-067003	11/231,773	ROBUST AND SCALABLE DE-SKEW METHOD FOR DATA PATH SKEW CONTROL	22-Sep-2005	012796/0045	John Ming Yung Chiang
0063-068003	10/144,725	A NETWORK DEICE HAVING A FLEXIBLE EEPROM FOR SETTING CONFIGURATION SETTINGS	15-May-2002	012900/0555	Wen-Cheng Tseng, Hsin-Min Yeh

0063-068004	10/965,211	FLEXIBLE APPARATUS FOR SETTING CONFIGURATIONS USING AN EEPROM	15-Oct-2004	012787/0130	Wen-Cheng Tseng, Hsin-Min Yeh
0063-071006	10/157,867	AGGREGATED RATE CONTROL METHOD AND SYSTEM	31-May-2002	012945/0471	David Wong
0063-231001	10/292,581	PHYSICAL LAYER LOOP BACK MEHOD AND APPARATUS	13-Nov-2002	013489/0835	David Wong, Xi Chen
0063-231002	11/384,516	PHYSICAL LAYER LOOP BACK MEHOD AND APPARATUS	21-Mar-2006	013489/0835	David Wong, Xi Chen
0063-232001	10/351,517	TOPOLOGY DISCOVERY FOR A NETWORK OF MANAGED DEVICES	27-Jan-2003	(Assignment 1-013708/0187) (Assignment 2-013789/0648)	Laxman Shankar
0063-076001	10/222,787	FAST FLEXIBLE FILTER PROCESSOR BASED ARCHITECTURE FOR A NETWORK DEVICE	19-Aug-2002	013205/0094	Shekhar Ambe, Shiri Kadambi, Sandeep Relan
0063-233001	10/234,165	A DISTRIBUTED 4-BIT INTERLEAVED PARITY (DIP4) CHECKER	05-Sep-2002	013264/0342	Ngok Ying Chu
0063-077001	10/238,805	AN EFFICIENT METHOD TO PARSE SPI-4 DATA STREAMS	11-Sep-2002	013283/0783	Ngok Ying Chu, John M. Chiang
0063-077002	11/702,578	AN EFFICIENT METHOD TO PARSE SPI-4 DATA STREAMS	06-Feb-2007	013283/0783	Ngok Ying Chu, John M. Chiang
0063-234001	10/234,267	MULTILEVEL PARSER FOR CONDITIONAL FLOW DETECTION IN A NETWORK DEVICE	05-Sep-2002	013264/0536	Laxman Shankar, Shekhar Ambe
0063-080001	10/268,668	FLOW CLASSIFICATION RULES COMBINATION	11-Oct-2002	013376/0293	Laxman Shankar
0063-235001	10/179,264	PACKET FILTERING BASED ON CONDITIONALEXPRESSION TABLE	26-Jun-2002	013048/0214	Sandeep Relan
0063-236001	10/191,529	FAST FLEXIBLE PACKET RANGE CHECKING	10-Jul-2002	013096/0377	Shekhar Ambe, Sandeep Relan
0063-085003	11/124,109	PER COS MEMORY PARTITIONING	09-May-2005	013299/0871	Laxman Shankar, Shekhar Ambe
0063-089001	10/163,991	DYNAMIC ADJUST MULTICAST DROP THRESHOLD TO PROVIDE FAIR HANDLING BETWEEN MULTICAST AND UNICAST FRAMES	07-Jun-2002	Assignment 1-012985/0559 Assignment 2-015571/0985	Shrjie Tzeng, Yi-Hsien Hao
0063-090001	10/237,612	BALANCED DUAL LINKED LIST FOR HIGH-PERFORMANCE PACKET BUFFERS	10-Sep-2002	013283/0688	Hyung Won Kim, Dennis S. Lee

0063-091001	10/283,134	SELF-REPAIRING BUILT-IN SELF-TEST FOR LINKED LIST MEMORIES	30-Oct-2002	013437/0943	Hyung Won Kim, Chuen-Shen Shung
0063-237001	10/269,988	PROGRAMMABLE INTER-PACKET GAP GENERATOR WITH BYTE GRANULARITY	15-Oct-2002	013389/0419	Johnson Yen, Michael Veng-Chong Lau
0063-238001	10/301,701	DESIGN OF A FAST FLEXIBLE PACKET PROCESSOR BASED ON RANGE CHECKING	22-Nov-2002	013514/0206	Sandeep Relan
0063-095001	10/358,315	FASTPATH IMPLEMENTATION FOR A DOUBLE TAGGING LOOPBACK ENGINE	05-Feb-2003	013742/0920	Laxman Shankar, Shekhar Ambe
0063-097001	09/397,911	APPARATUS AND METHOD FOR UNILATERAL TOPOLOGY DISCOVERY IN NETWORK MANAGEMENT	17-Sep-1999	Assignment 1-010614/0119 Assignment 2-013609/0288 Assignment 3-013609/0381	KIM K. BANKER, CHRISTOPHER ALAN DEL SIGNORE, GAVIN BOWLBY, RICHARD KARL FELDMAN et al
0063-098010	10/348,687	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COST	21-Jan-2003	Assignment 1-010028/0727 Assignment 2-013609/0300	Alistair d. Black, Kurt Chan
0063-098011	10/348,648	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COST	21-Jan-2003	Assignment 1-010028/0727 Assignment 2-013609/0300	Alistair d. Black, Kurt Chan
0063-098012	10/349,045	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COST	21-Jan-2003	Assignment 1-010028/0727 Assignment 2-013609/0300	Alistair d. Black, Kurt Chan
0063-098013	12/292,440	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COST	19-Nov-1998	Assignment 1-010028/0727 Assignment 2-013609/0300	Alistair d. Black, Kurt Chan
0063-099001	09/384,496	DIGITAL SUBCHANNEL TRANSCEIVER FOR TRANSMITTING DATA	27-Aug-1999	Assignment 1-010614/0132 Assignment 2-013609/0378	RICHARD KARL FELDMAN, FARIVAR FARZANEH, MICHAEL TIMOTHY KAUFFMAN
0063-100001	09/881,949	SWITCH ASSISTED FRAME ALIASING FOR STORAGE VIRTUALIZATION	19-Jan-2005	Assignment 1-012149/0425 Assignment 2-013609/0464	Lani William Brewer, John Gifford Logan
0063-100002	11/037,197	SWITCH ASSISTED FRAME ALIASING FOR STORAGE VIRTUALIZATION	19-Jan-2005	Assignment 1-012149/0425 Assignment 2-013609/0464	Lani William Brewer, John Gifford Logan
0063-239002	10/879,588	BANDPASS FILTER WITH INTEGRATED VARIABLE GAIN FUNCTION USING IMPROVED RESISTOR ARRAY	30-Jun-2004	015536/0108	Meng-An Pan

0063-24002	11/226,435	OSCILLATOR WITH QUADRATURE OUTPUT IN A CROSS-COUPLED CONFIGURATION	15-Sep-2005	015937/0791	Hung-Ming Chien
0063-122002	11/384,515	A QUADBAND CMOS RECEIVER FOR GSM/GSM850/PCS/DCS CELLUAR SYSTEM APPLICATION	21-Mar-2006	015273/0249	Janice Chiu, Hooman Darabi
0063-241001	11/117,515	LOW COST COAXIAL CABLE CONNECTION FOR WIRELESS ANTENNAS	29-Apr-2005	016520/0258	William B. Higgins
0063-242001	10/927,474	GRAPHICAL TEST DEVELOPMENT TOOL FOR USE WITH AUTOMATED TEST EQUIPMENT	27-Aug-2004	015738/0977	Steven L. Petersen, Tyler L. Tolman
0063-243001	10/951,793	NON-MATCH POWER AMPLIFIER AND METHOD FOR INCREASED OUTPUT 1 DB COMPRESSION POINT	29-Sep-2004	015849/0864	Meng-An Pan
0063-244001	10/953,362	DIFFERENTIAL POWER AMPLIFIER AND METHOD IN CLASS AB MODE	30-Sep-2004	015857/0258	Meng-An Pan
0063-245001	11/099,530	A SYSTEM AND METHOD FOR MAINTAINING A LAYER 2 MODIFICATION BUFFER	06-Apr-2005	016448/0869	Michael J. Bowes, Eric A. Baden, John Jeffrey Dull, Curt McDowell
0063-246001	11/204,113	SLICER WITH LARGE INPUT COMMON MODE RANGE	16-Aug-2005	016900/0868	Behnam Mohammadi, Hooman Darabi